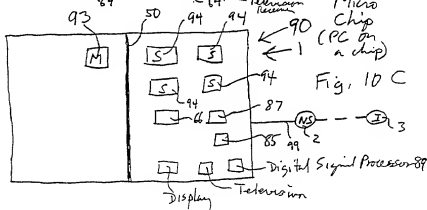


Micro Chip  
(PC on a chip)

Fig. 10C



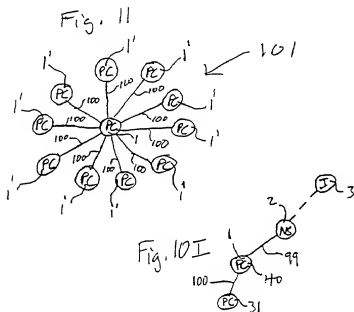
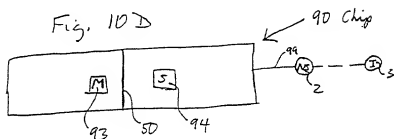
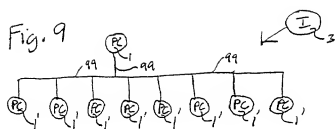


Fig. 10E

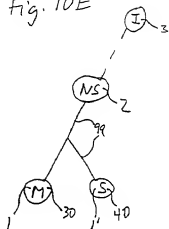


Fig. 10F

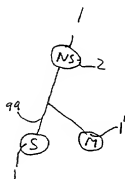


Fig. 10G

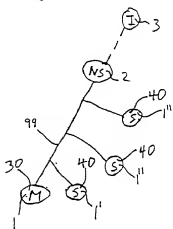
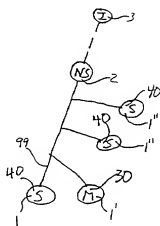
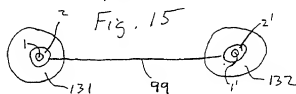
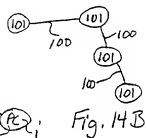
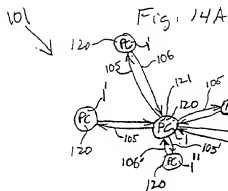
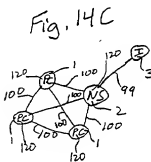
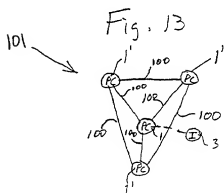
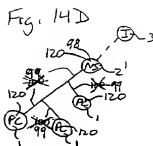
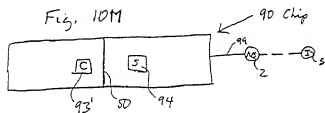
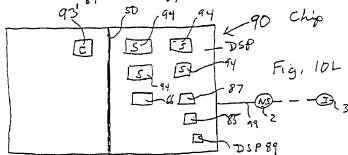
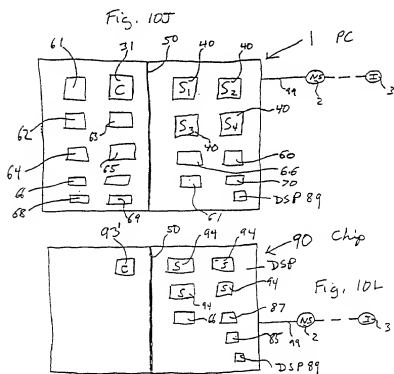
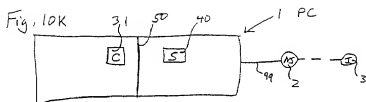


Fig. 10H







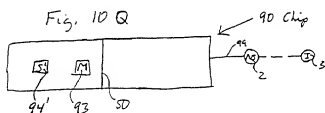
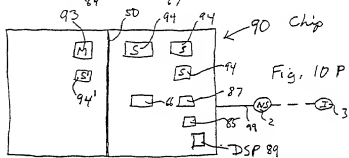
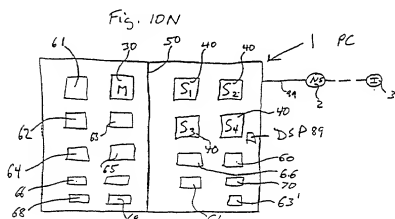
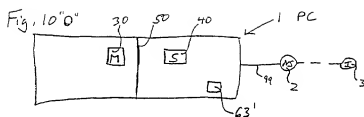




Fig. 16 A



Fig. 16 B

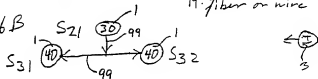


Fig. 16 C

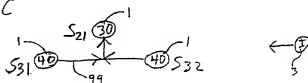


Fig. 16 D



Fig. 16 E

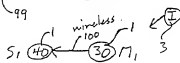


Fig. 16 F

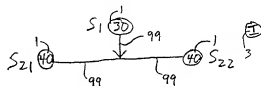


Fig. 16 G

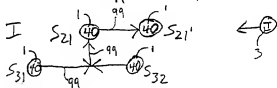


Fig. 16 H



Fig. 16 E

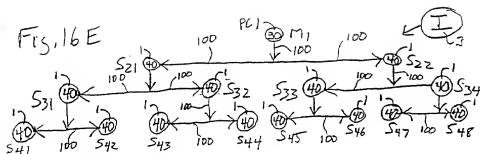


Fig. 16 F

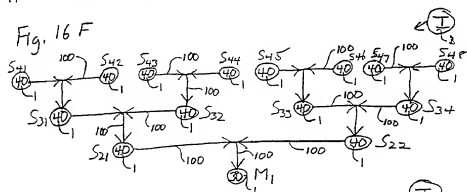


Fig. 16 K

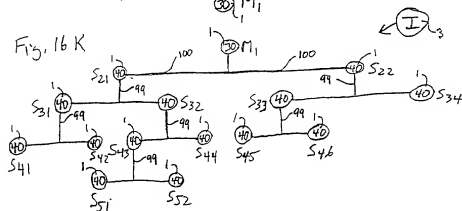


Fig. 16L

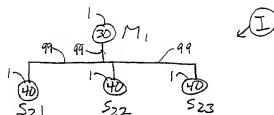


Fig. 16M

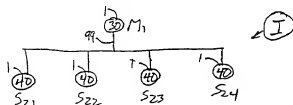


Fig. 16N

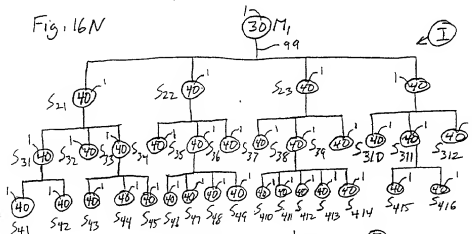


Fig. 16O

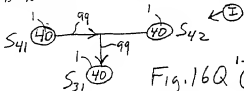


Fig. 16Q

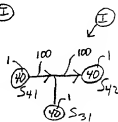


Fig. 16P

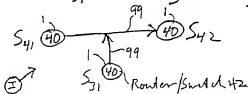


Fig. 16R

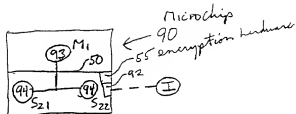


Fig. 16S

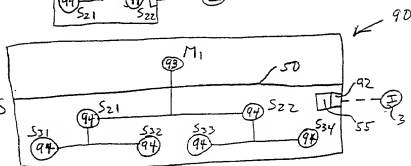


Fig. 16T

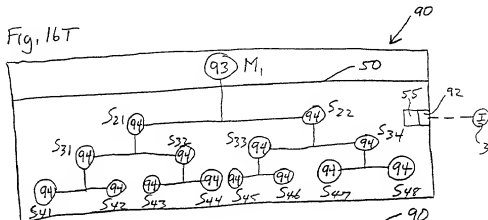
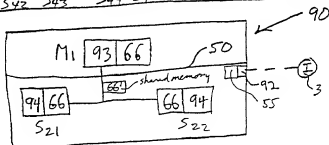


Fig. 16U



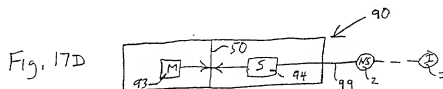
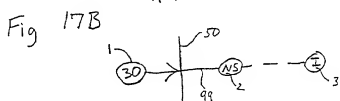
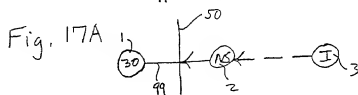
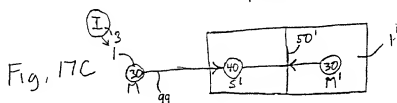
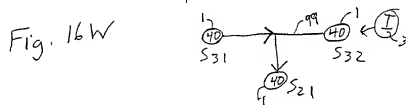
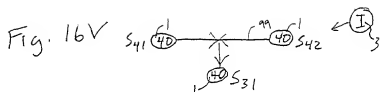


Fig. 16X

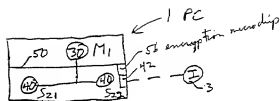


Fig. 16Y

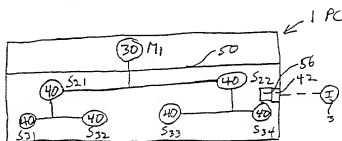


Fig. 16Z

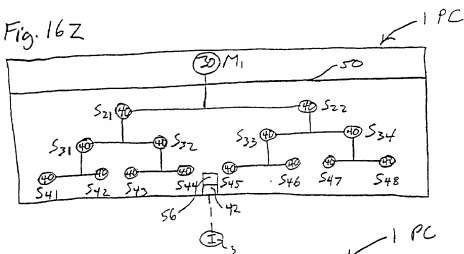


Fig. 16AA

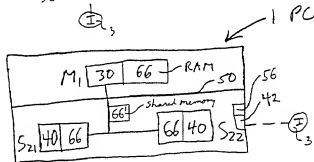


Fig. 18A

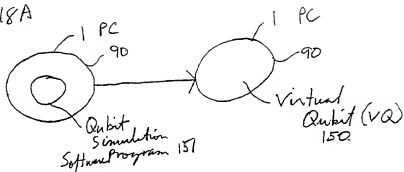


Fig. 18B

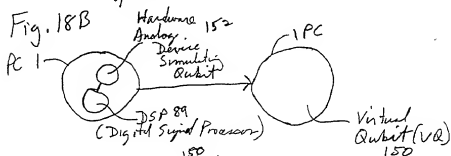


Fig. 18C

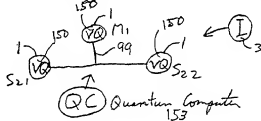


Fig. 18D

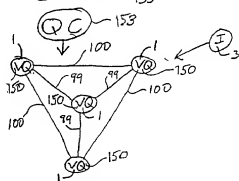






Fig. 20A

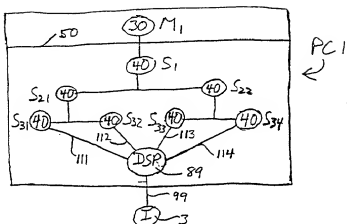


Fig. 20B

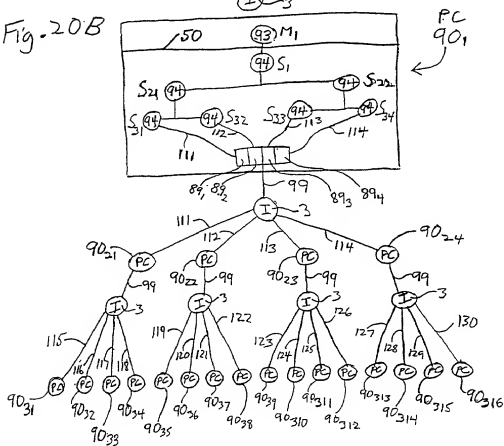


Fig. 21A

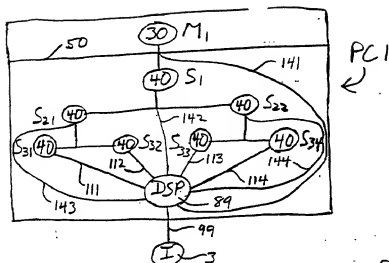


Fig. 21B

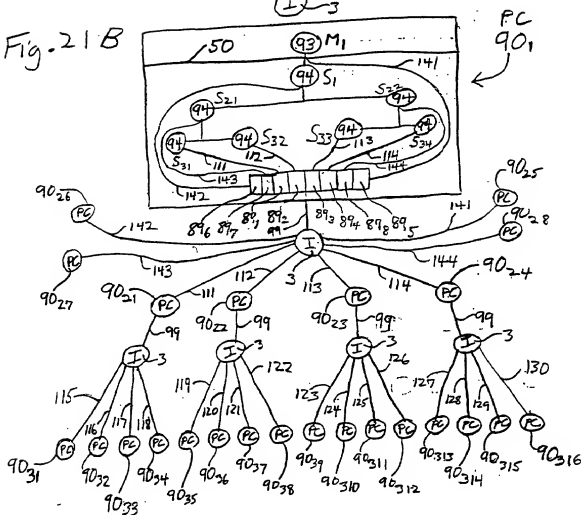


Fig. 22A

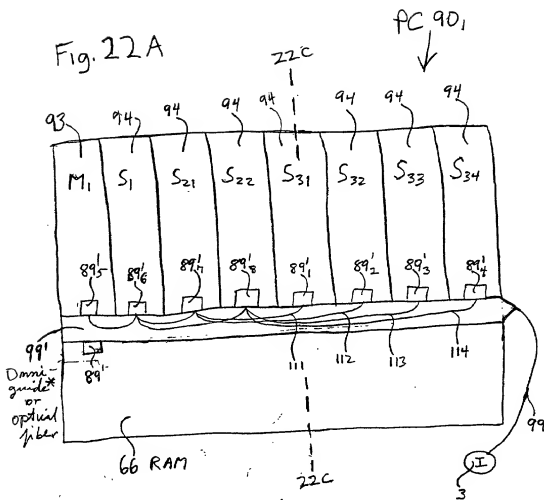


FIG. 22B

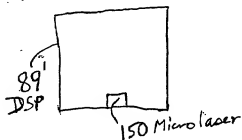


FIG. 22C

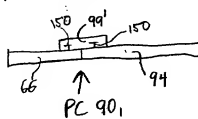


Fig. 23A

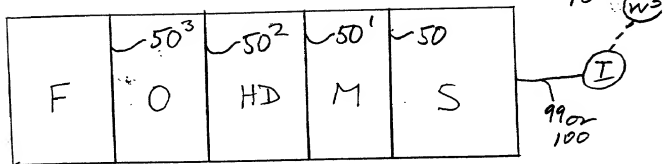


Fig. 23B

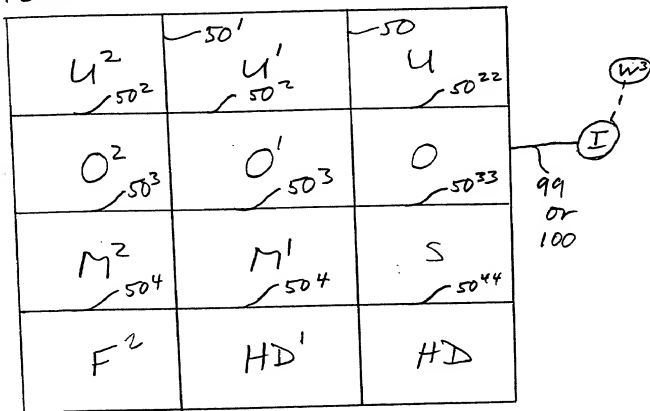


Fig. 2.4

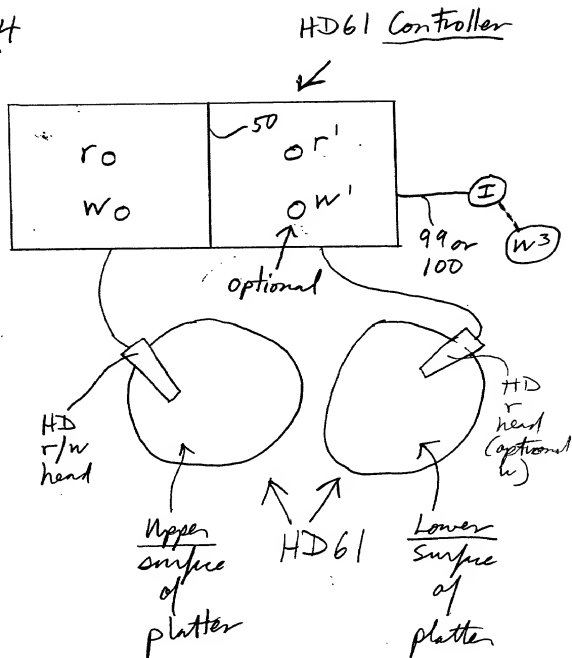


Fig. 23C

PC1 or PC 90

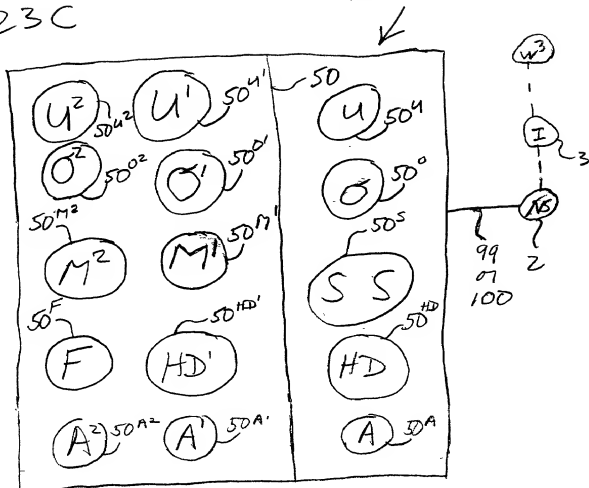


Fig. 23D

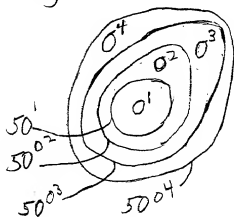


Figure 23E

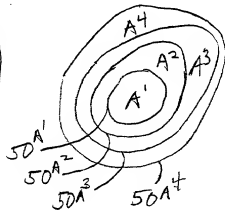


Fig. 25A

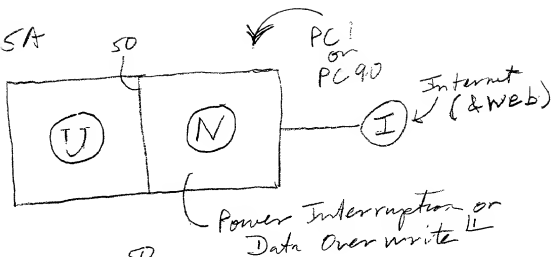


Fig. 25B

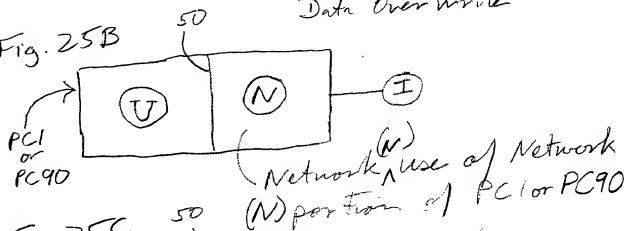


Fig. 25C

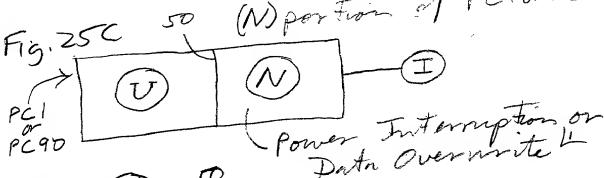


Fig. 25D

